

Fig.1

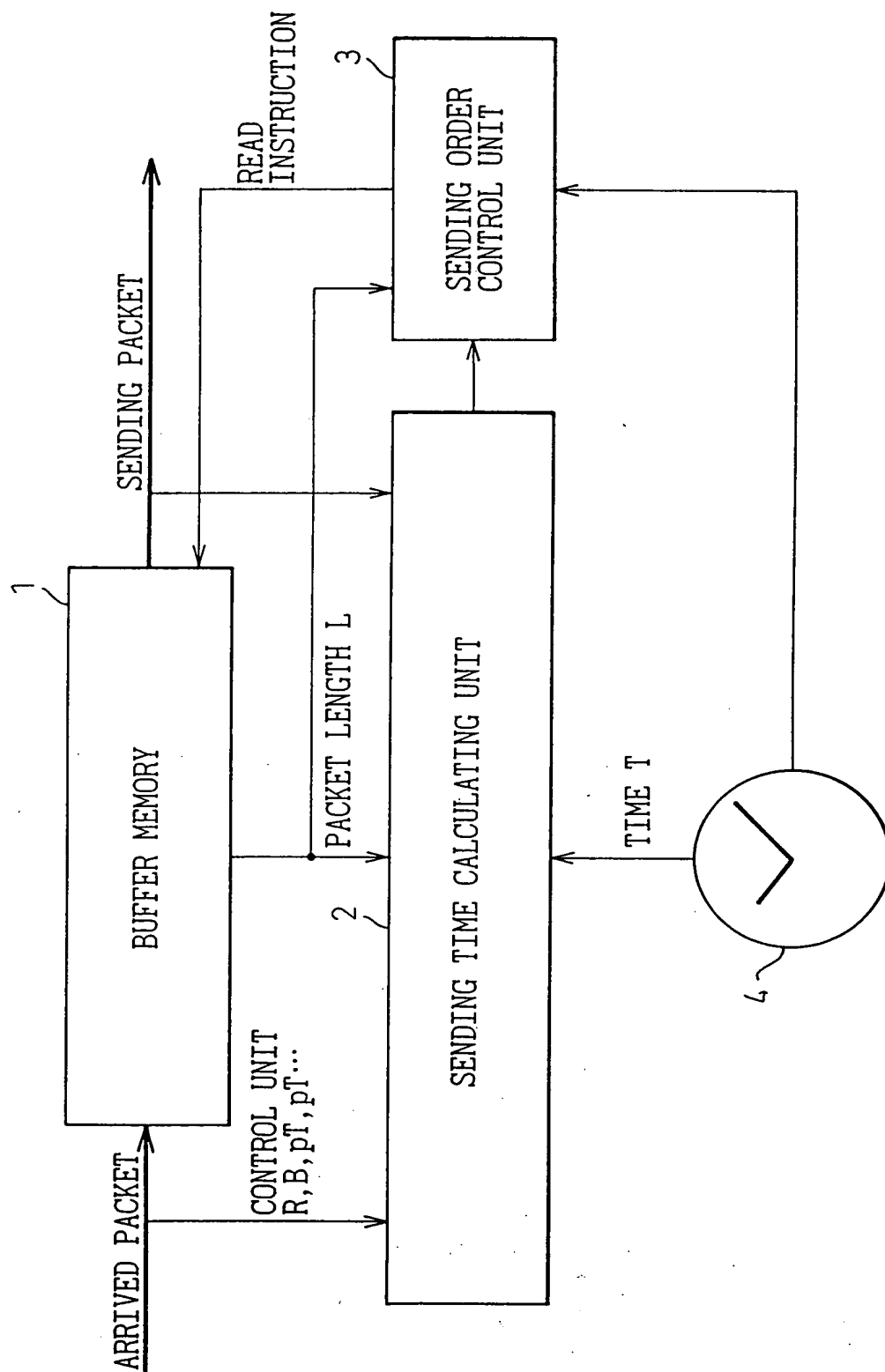


Fig.2

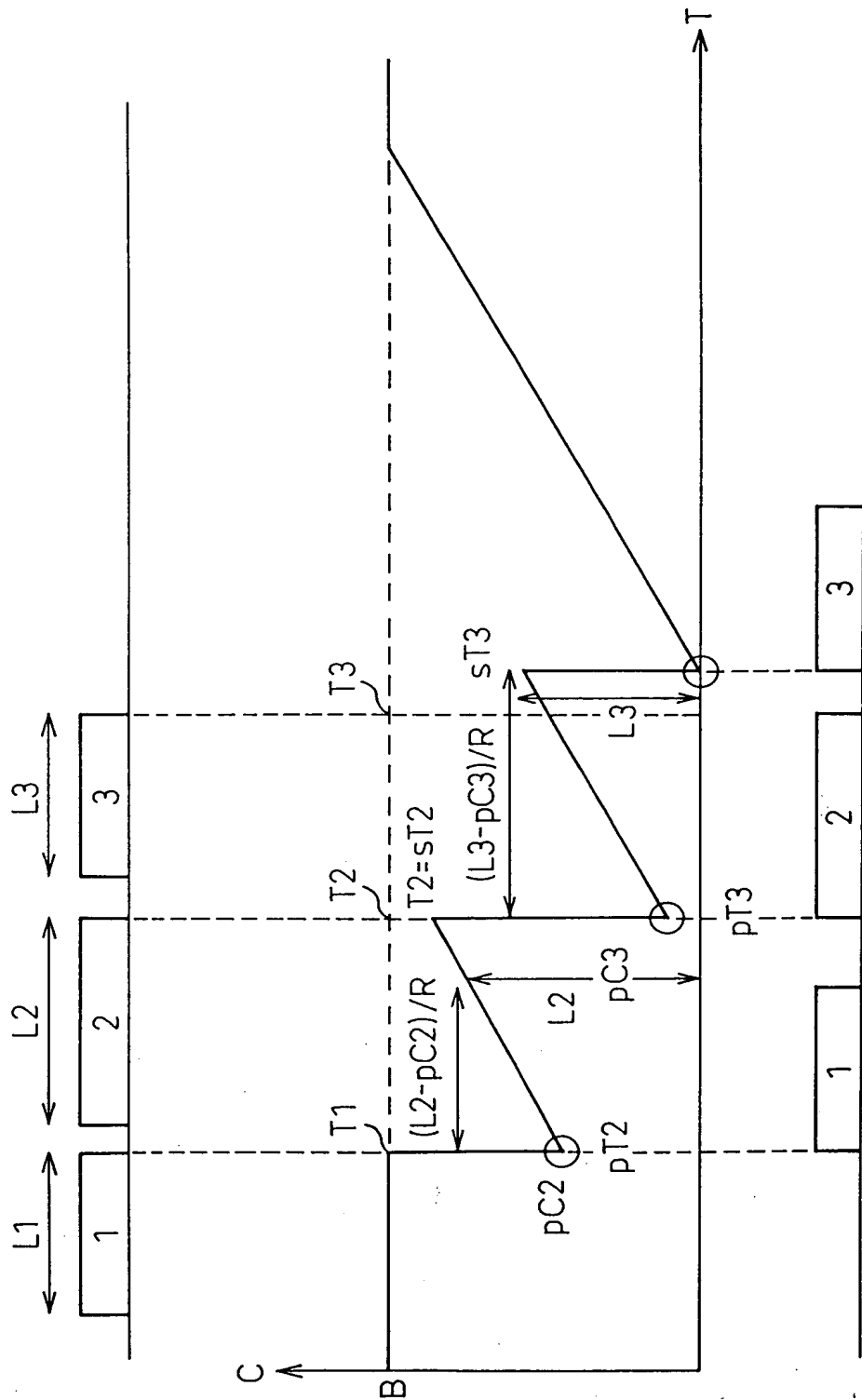


Fig.3

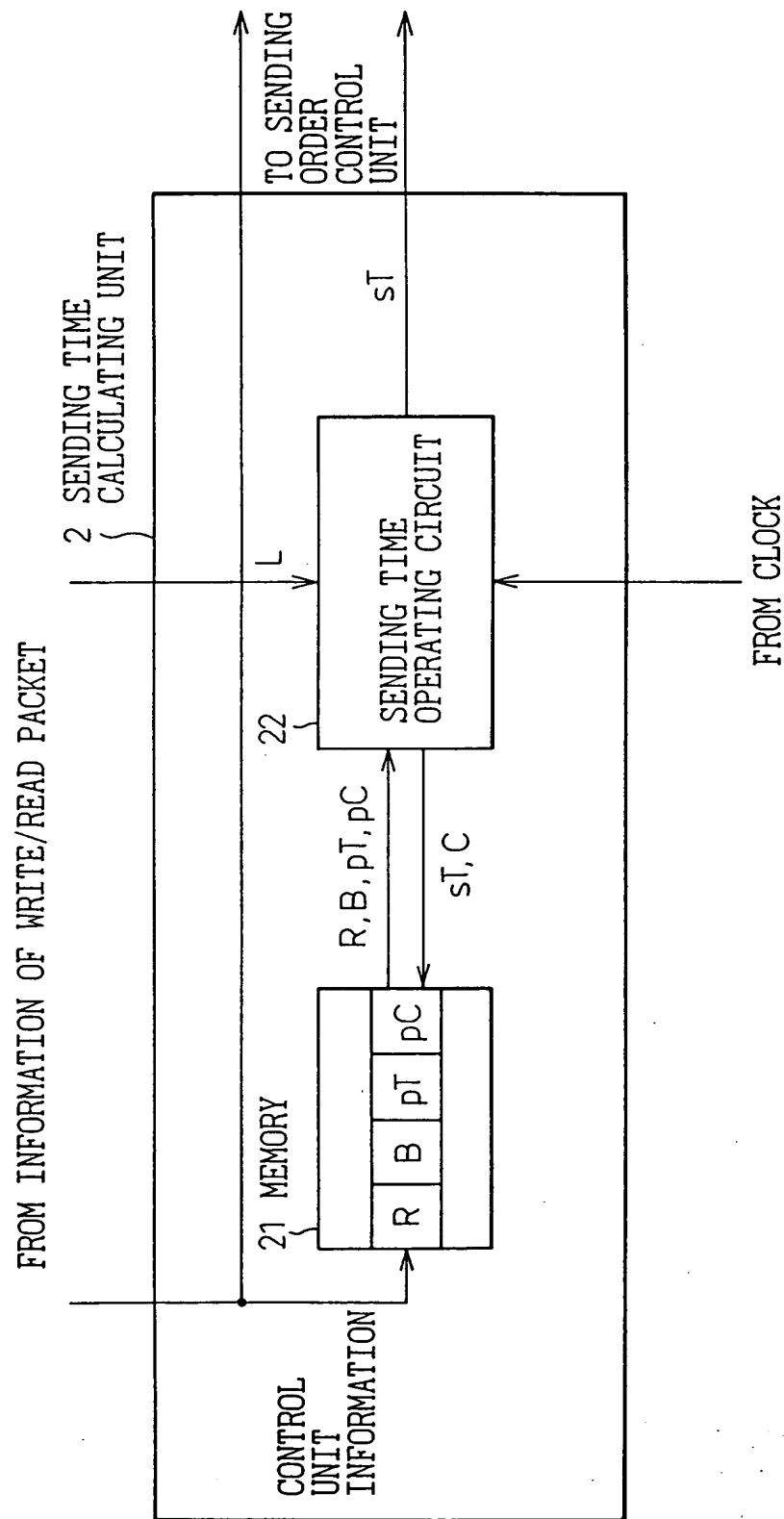
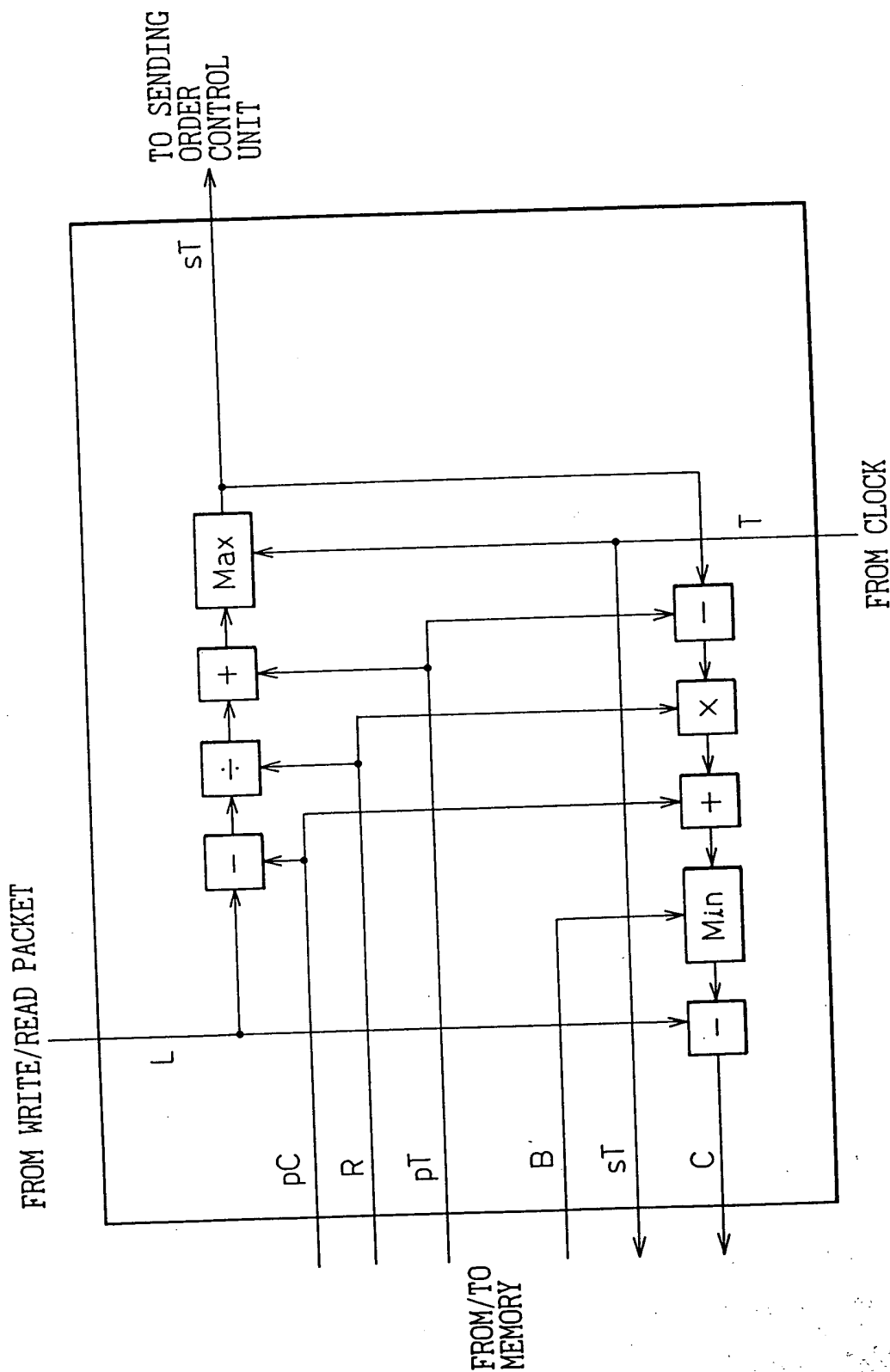


Fig. 4



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Fig.5

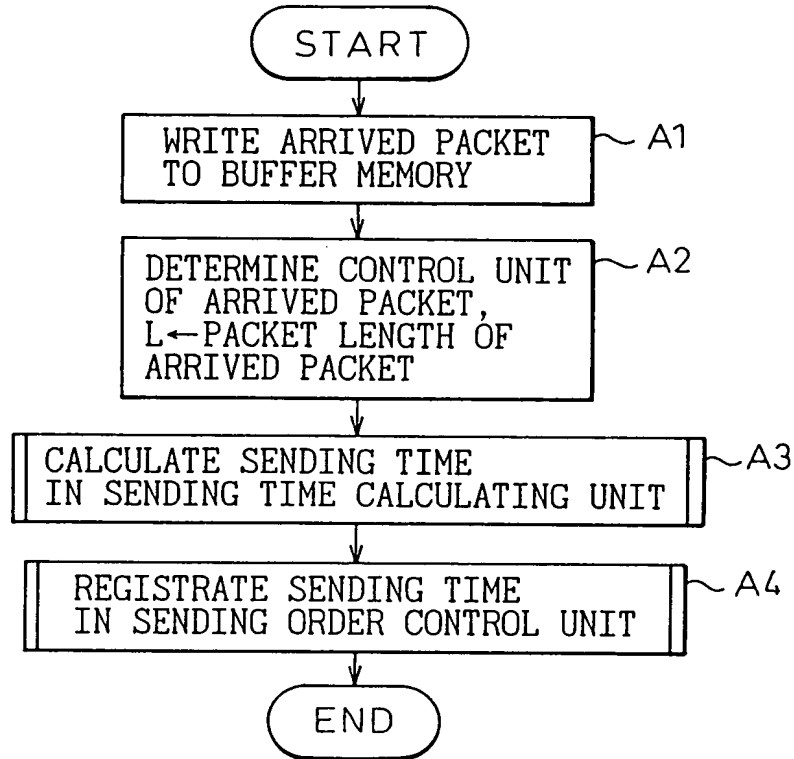


Fig.6

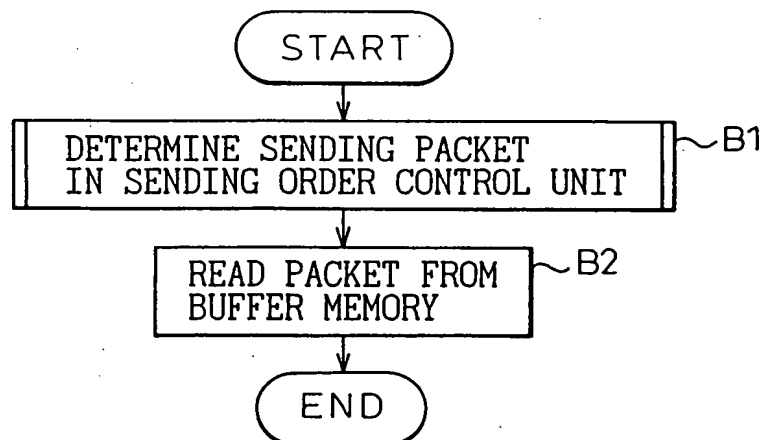
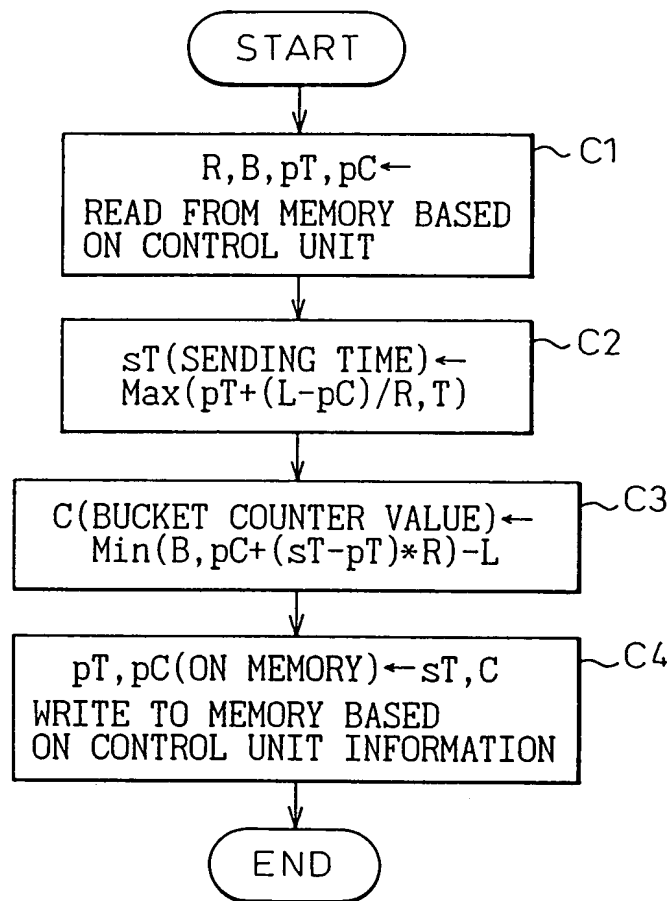


Fig.7



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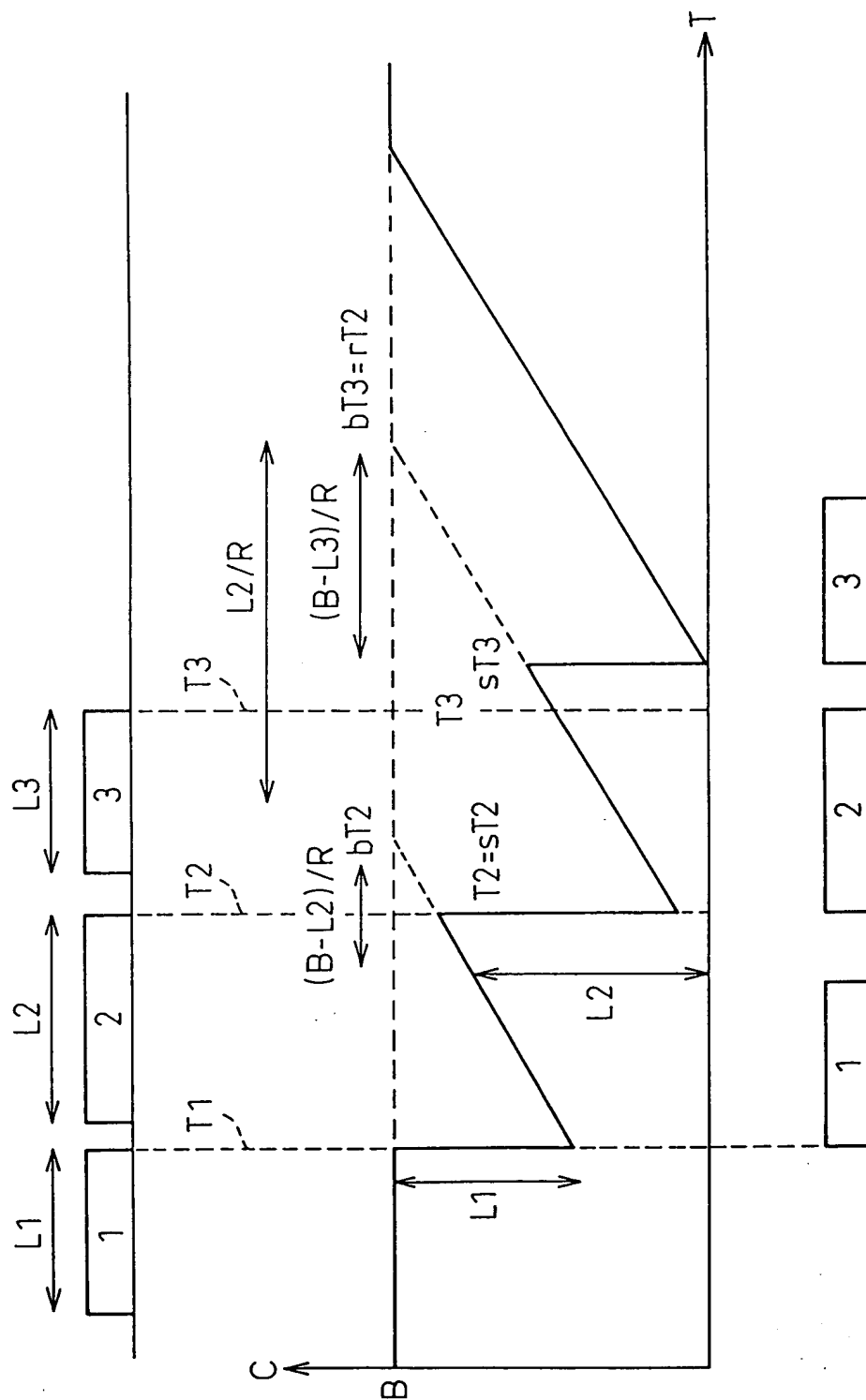


Fig.9

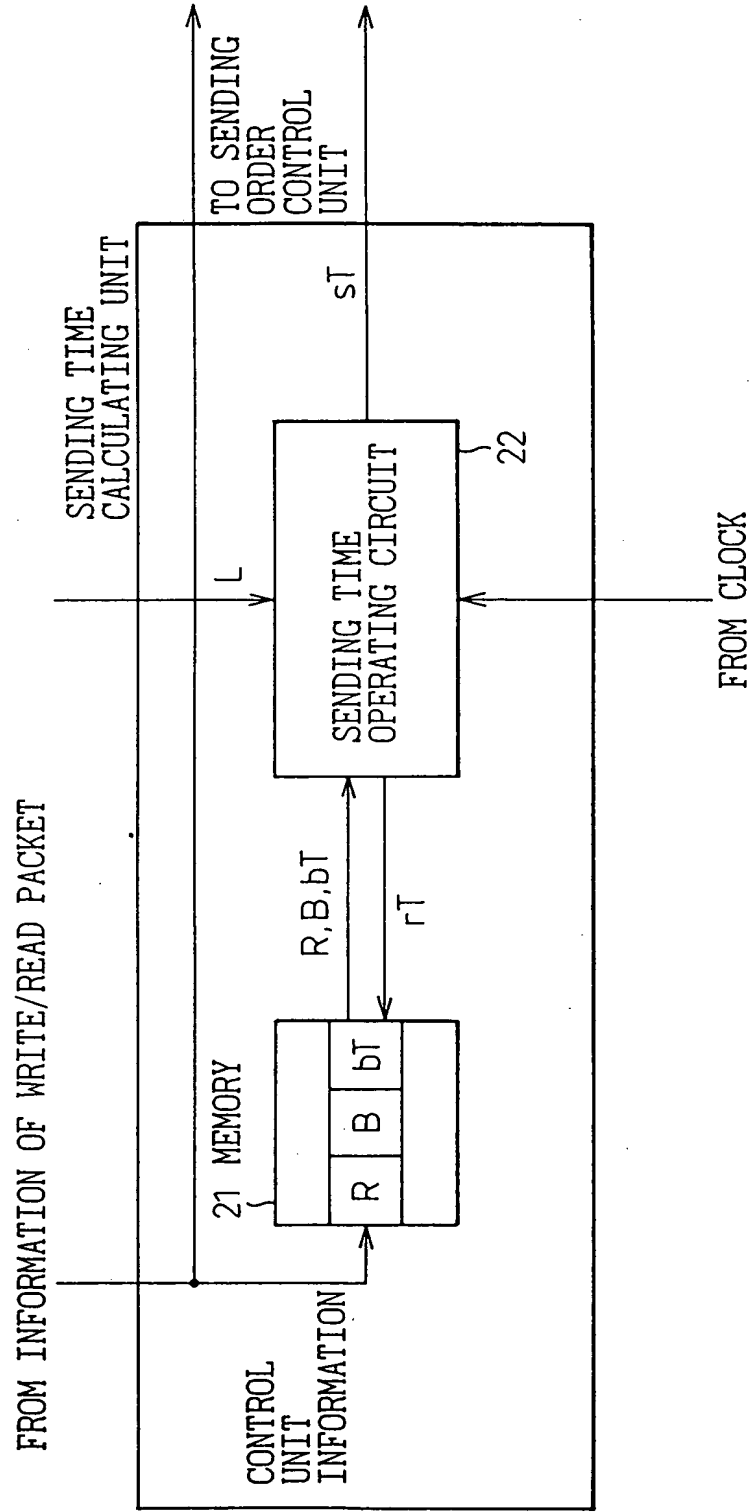


Fig.10

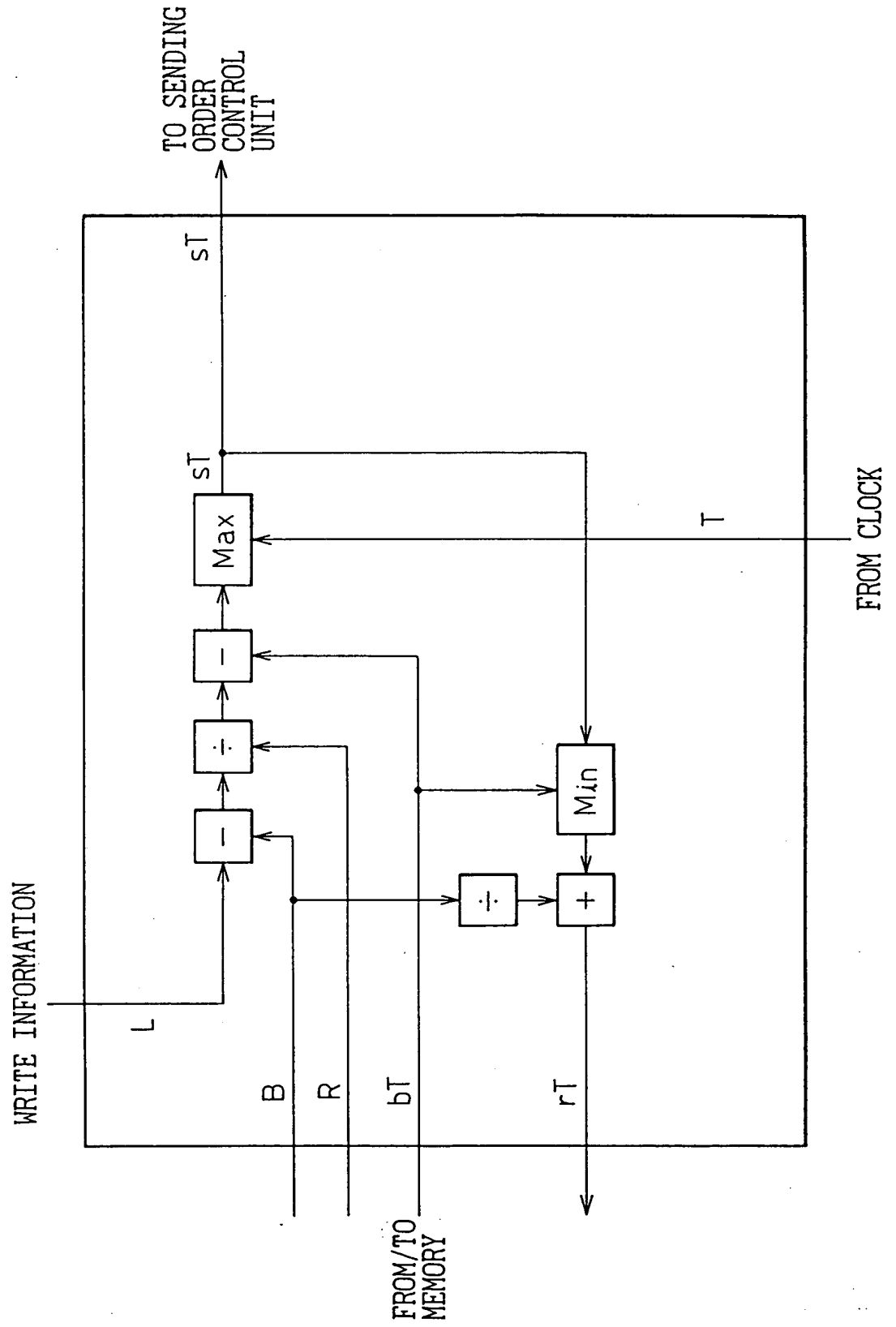
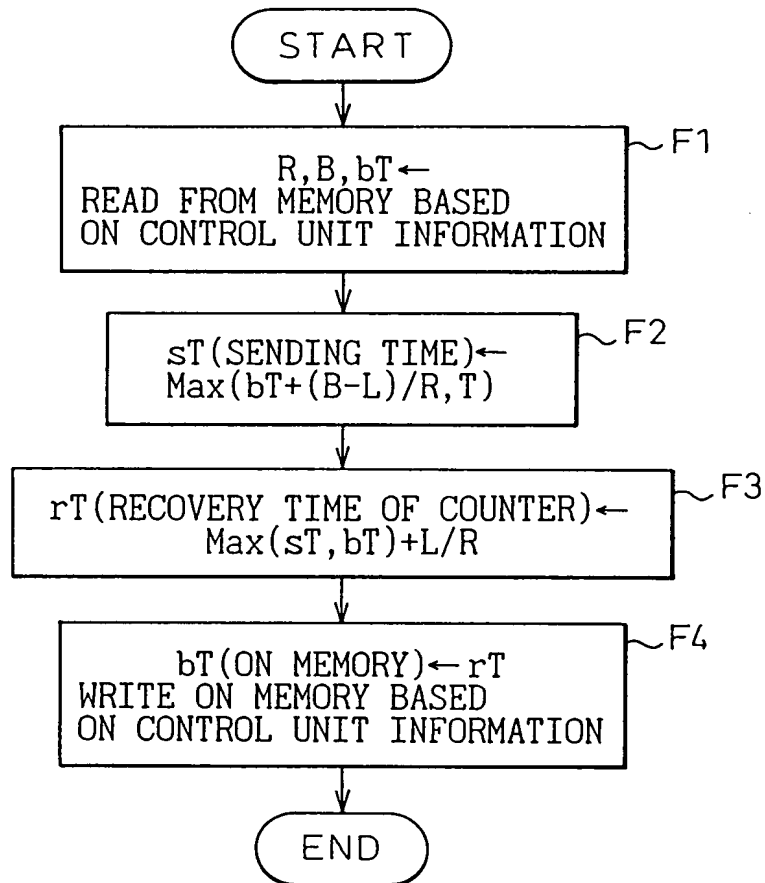
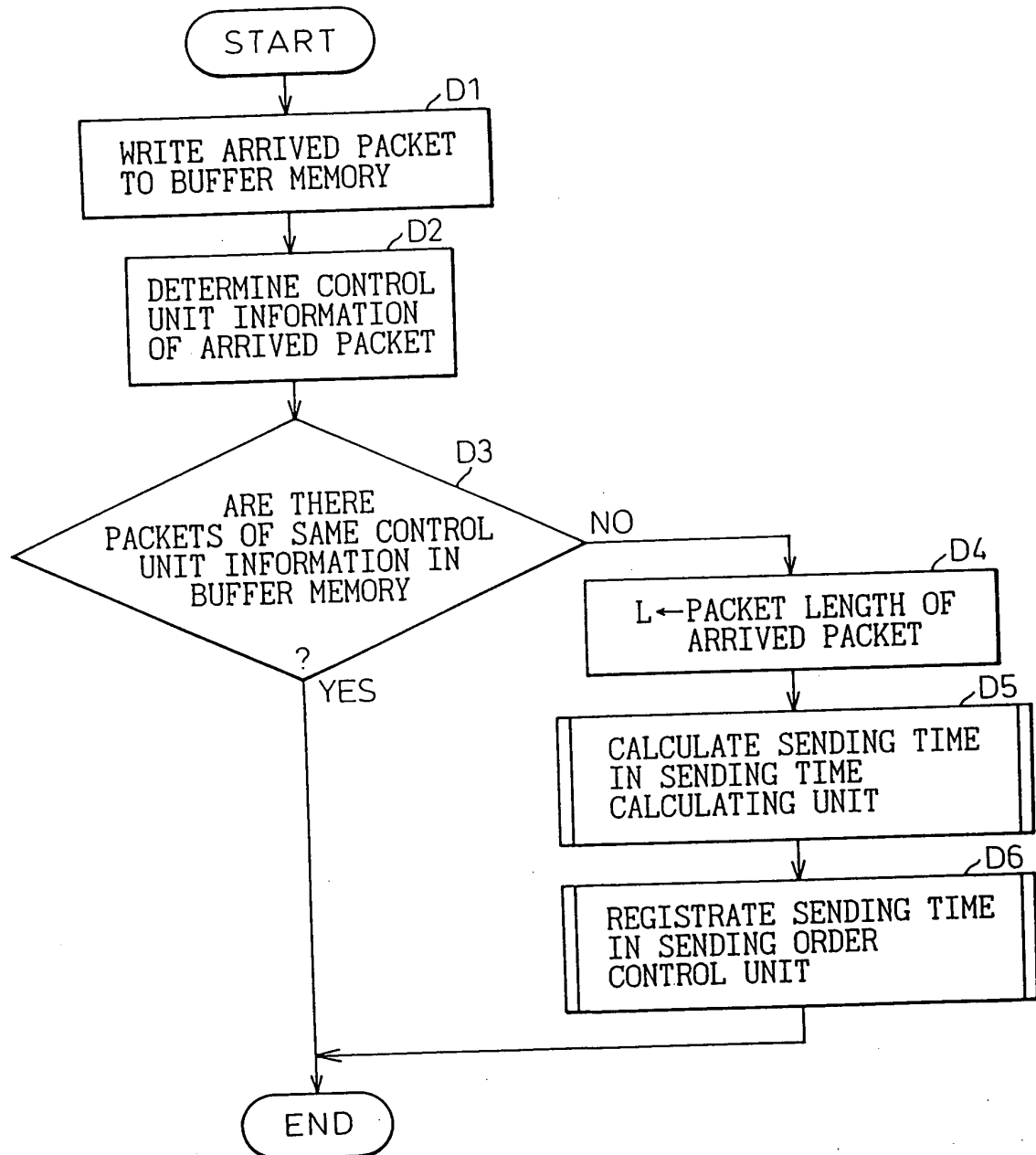


Fig.11



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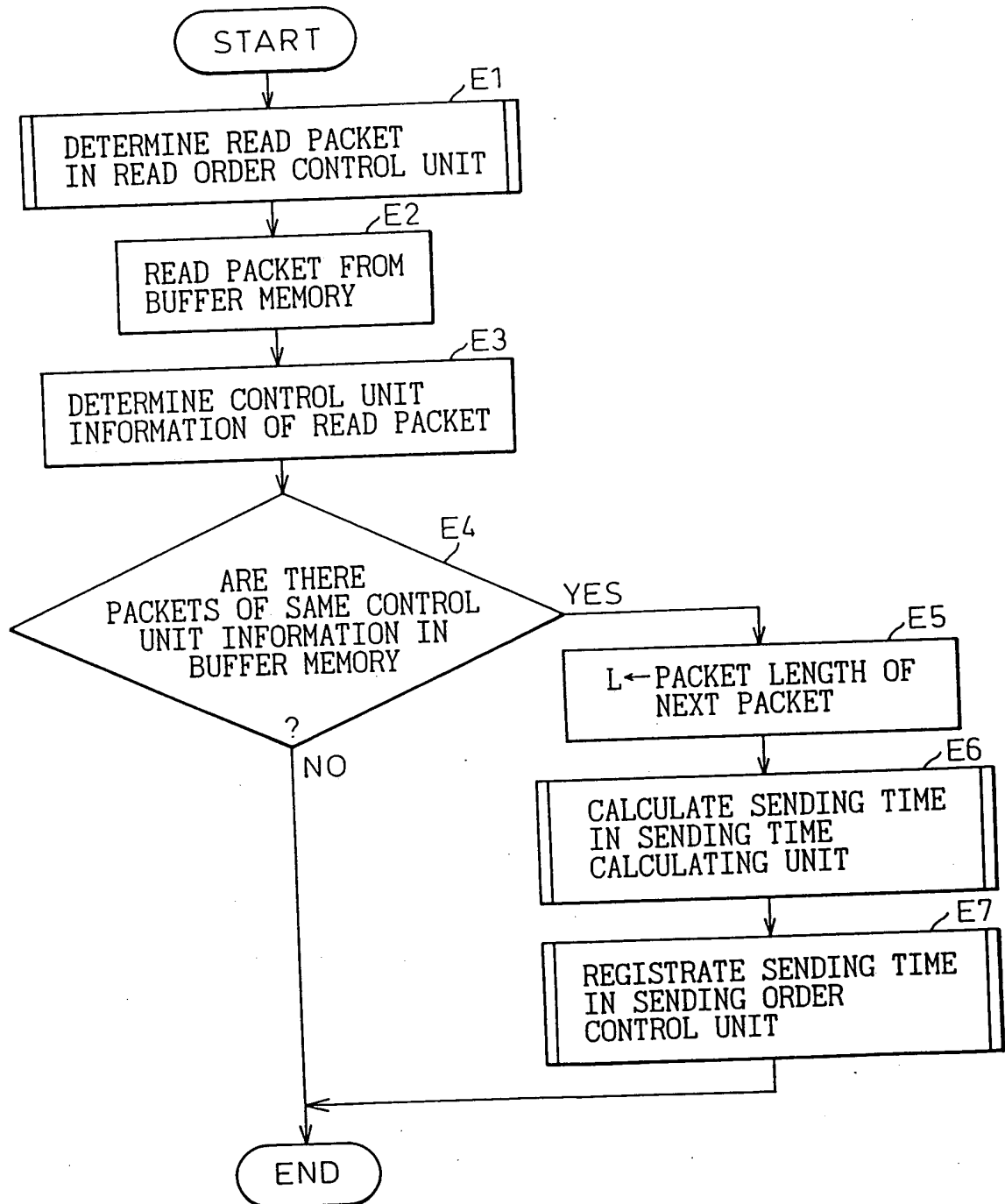
Fig.12



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Fig.13



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Fig.14

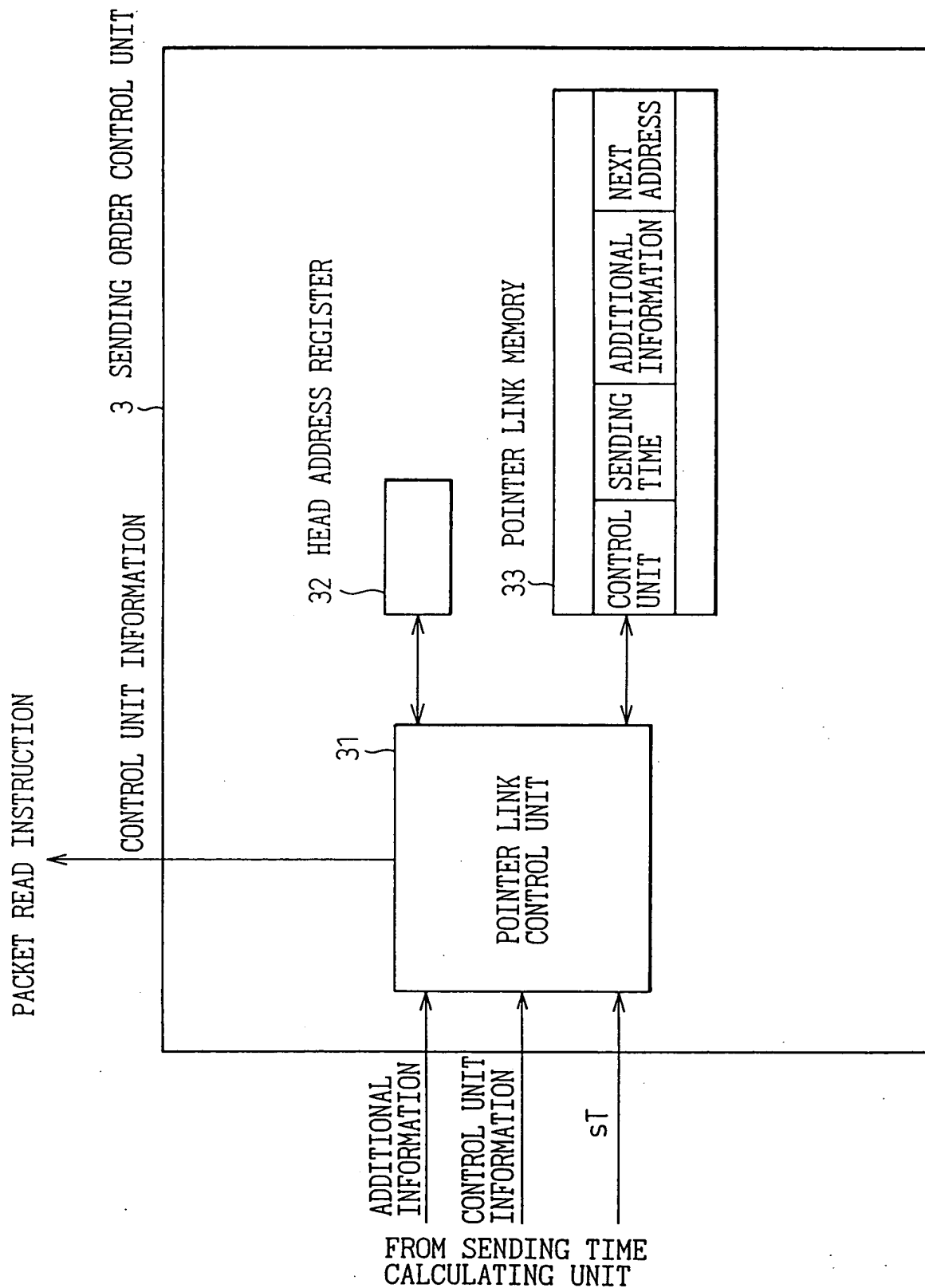


Fig. 15

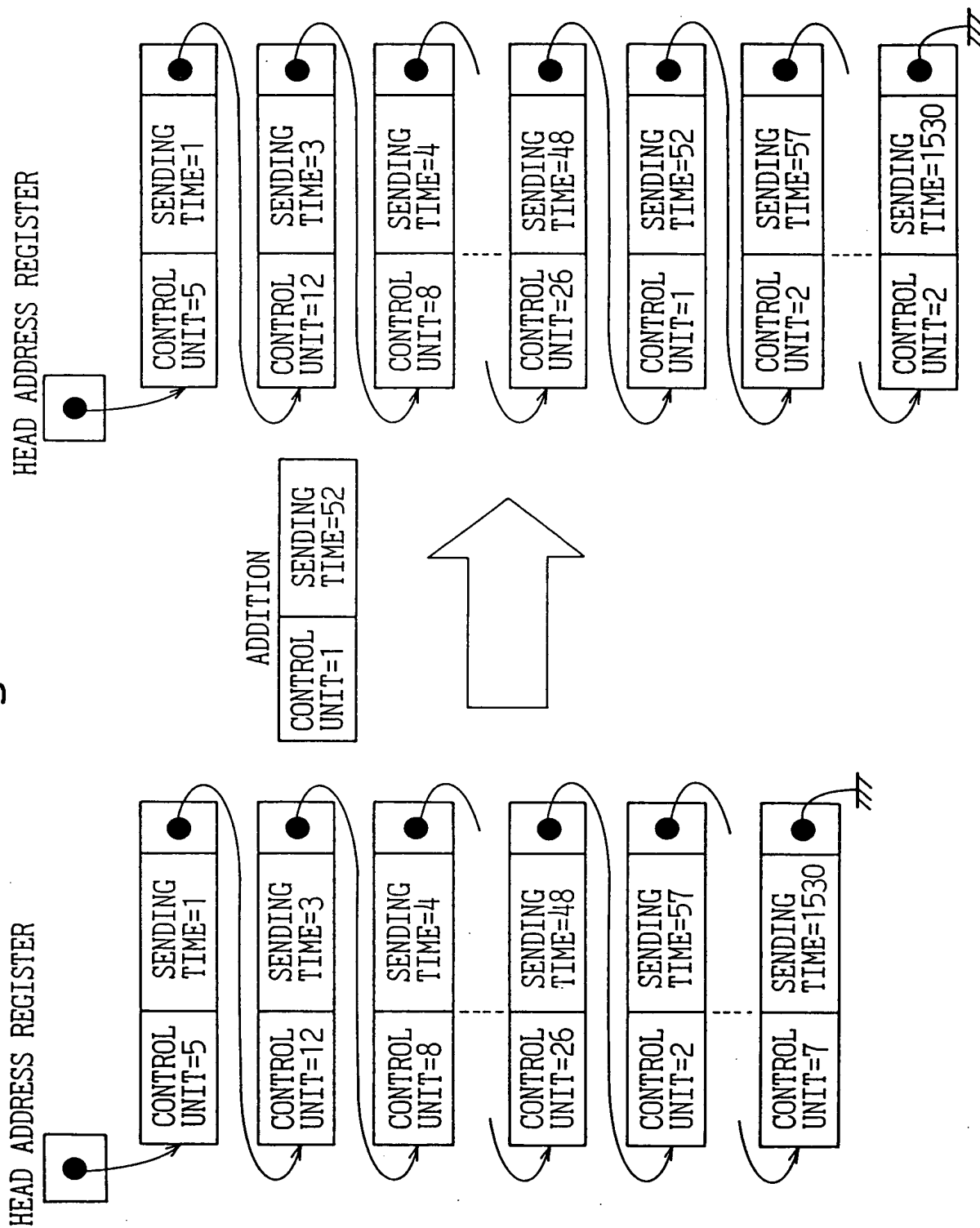


Fig.16

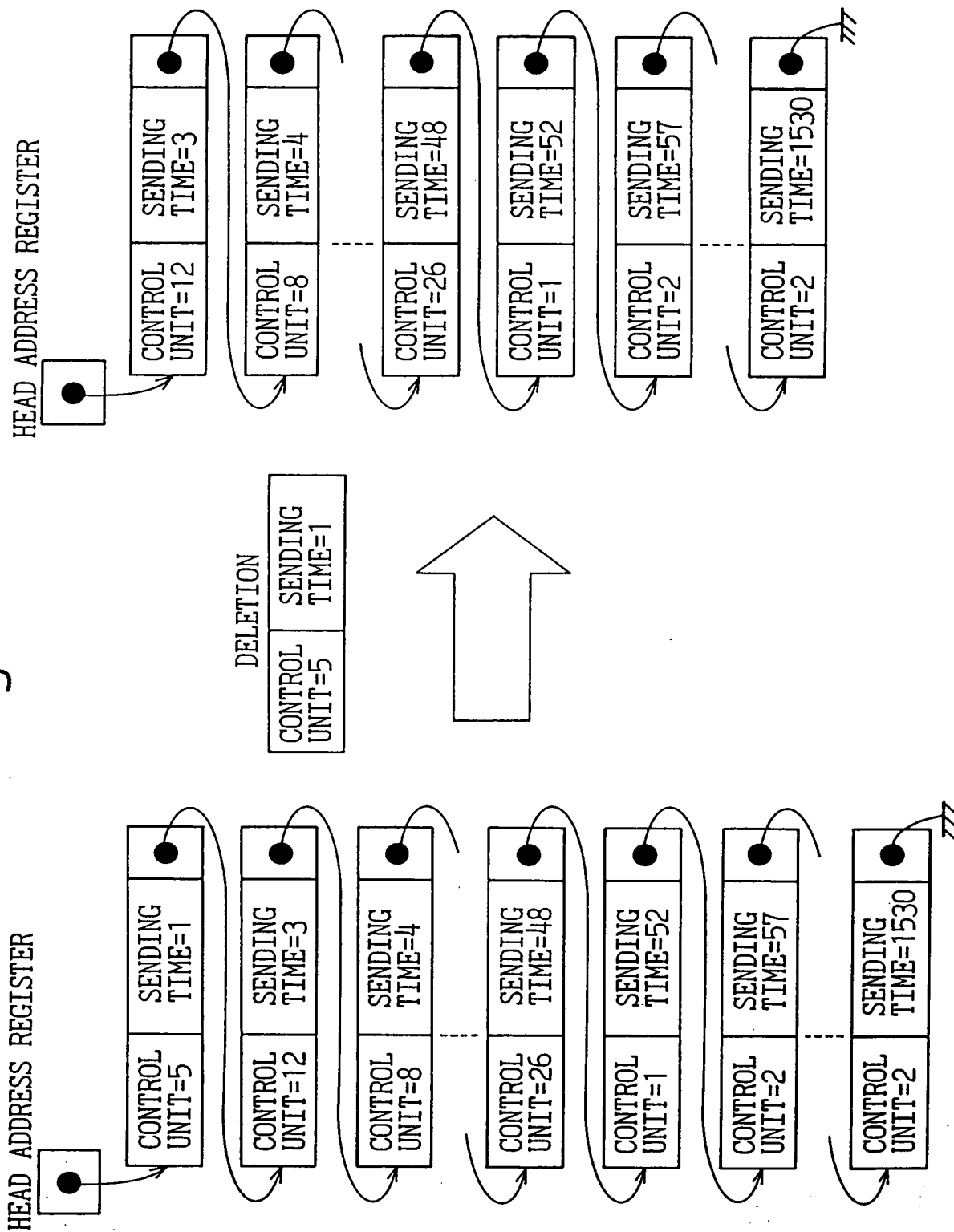
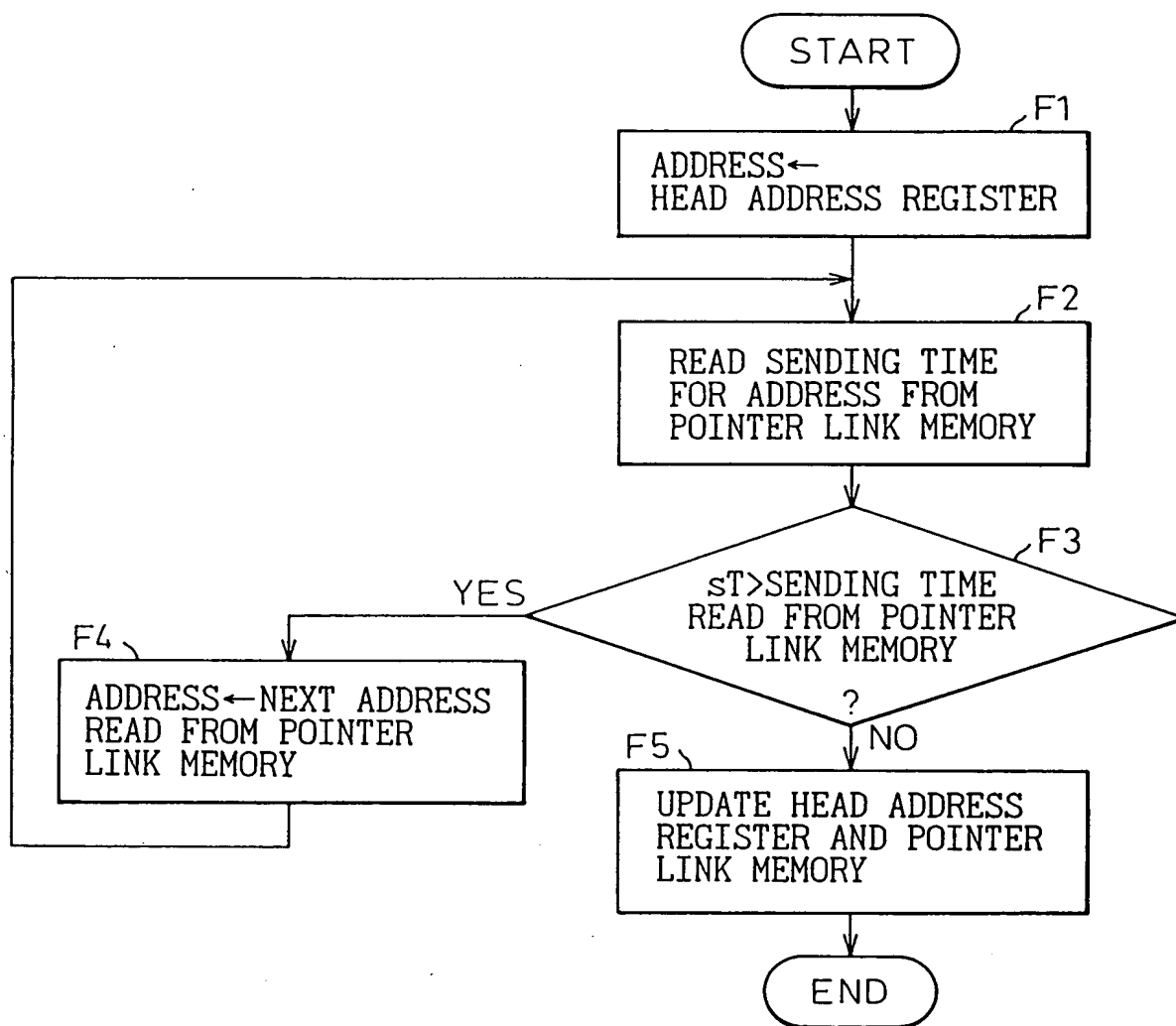


Fig.17



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graph TD
    START([START]) --> G1[G1]
    G1 --> READ1[READ CONTENTS OF HEAD ADDRESS REGISTER FROM POINTER LINK MEMORY]
    READ1 --> G2[G2]
    G2 --> DEC1{ $T < \text{SENDING TIME READ FROM POINTER LINK MEMORY}$ }
    DEC1 -- YES --> G1
    DEC1 -- NO --> G3[G3]
    G3 --> READ2[INFORM CONTROL UNIT READ FROM POINTER LINK MEMORY TO BUFFER MEMORY]
    READ2 --> G4[G4]
    G4 --> REPLACE[REPLACE CONTENTS OF HEAD ADDRESS REGISTER TO NEXT ADDRESS READ FROM POINTER LINK MEMORY]
    REPLACE --> END([END])
  
```

Fig.19

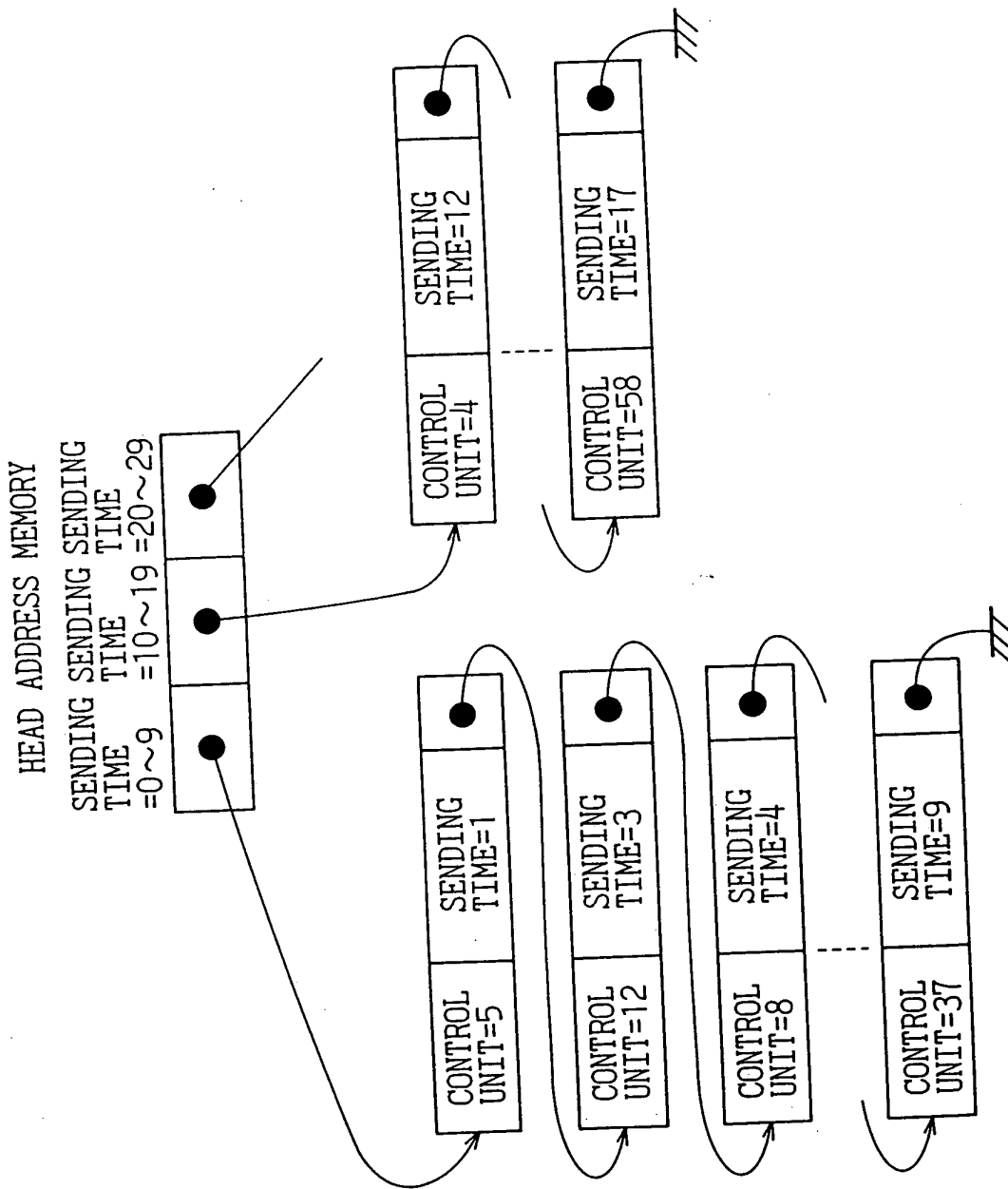


Fig. 20

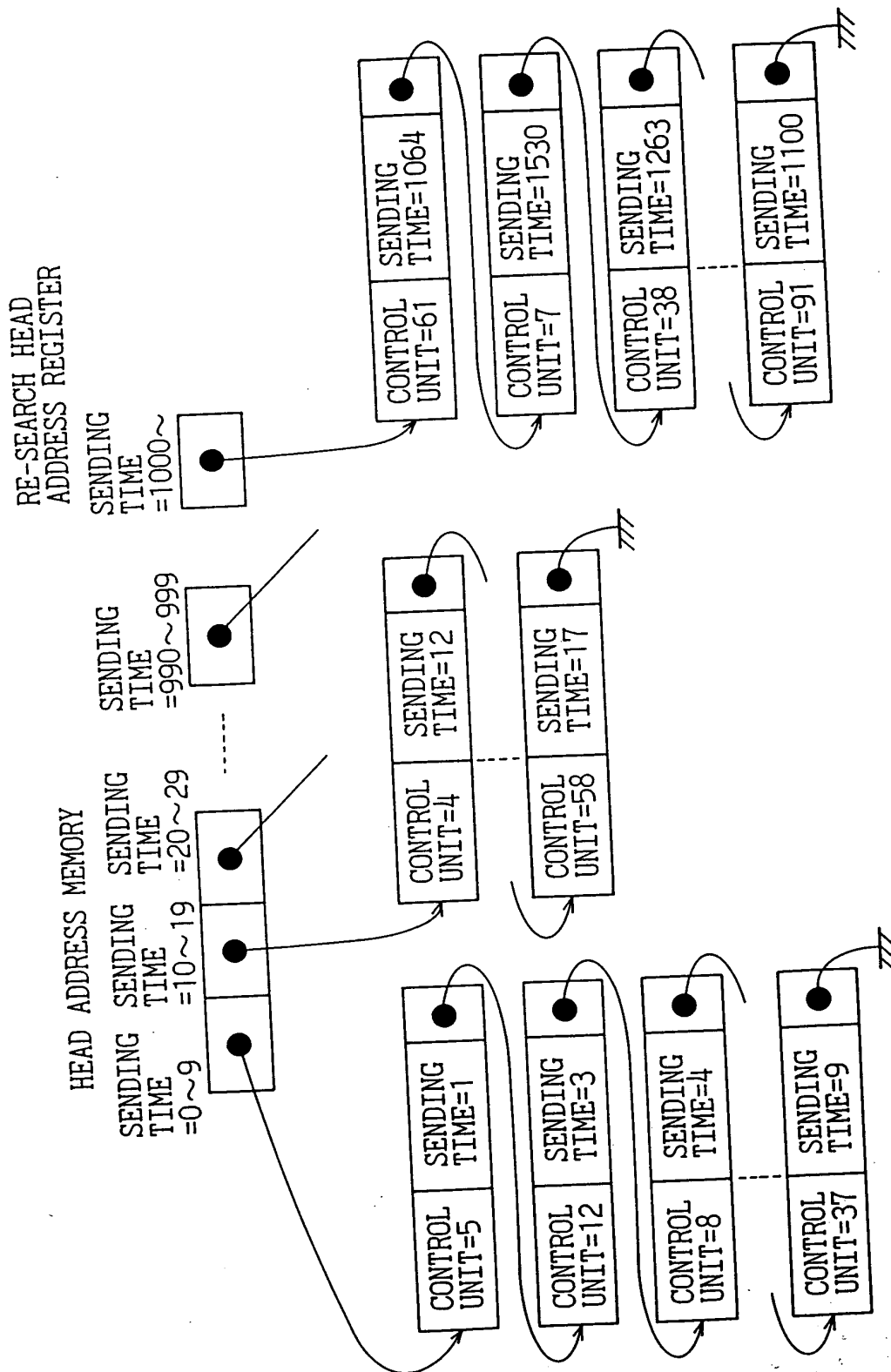
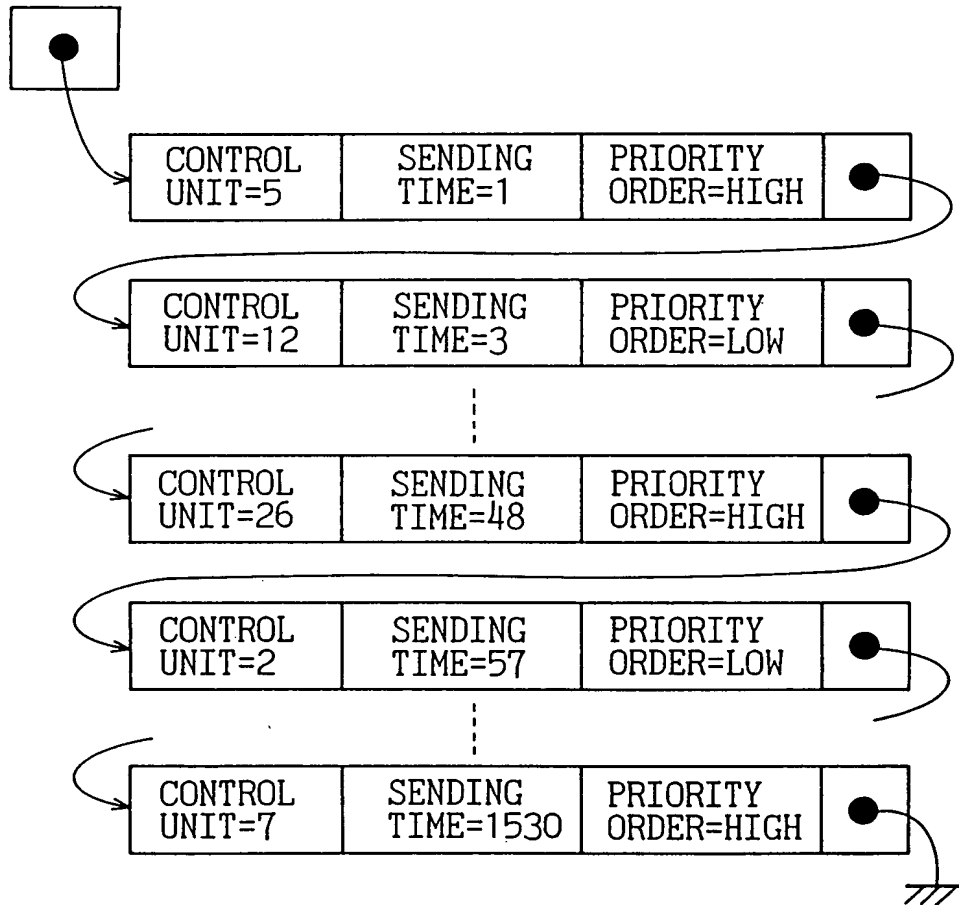


Fig.21

HEAD ADDRESS REGISTER



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Fig.22

HEAD ADDRESS REGISTER

